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# Interconnect Fabric Module

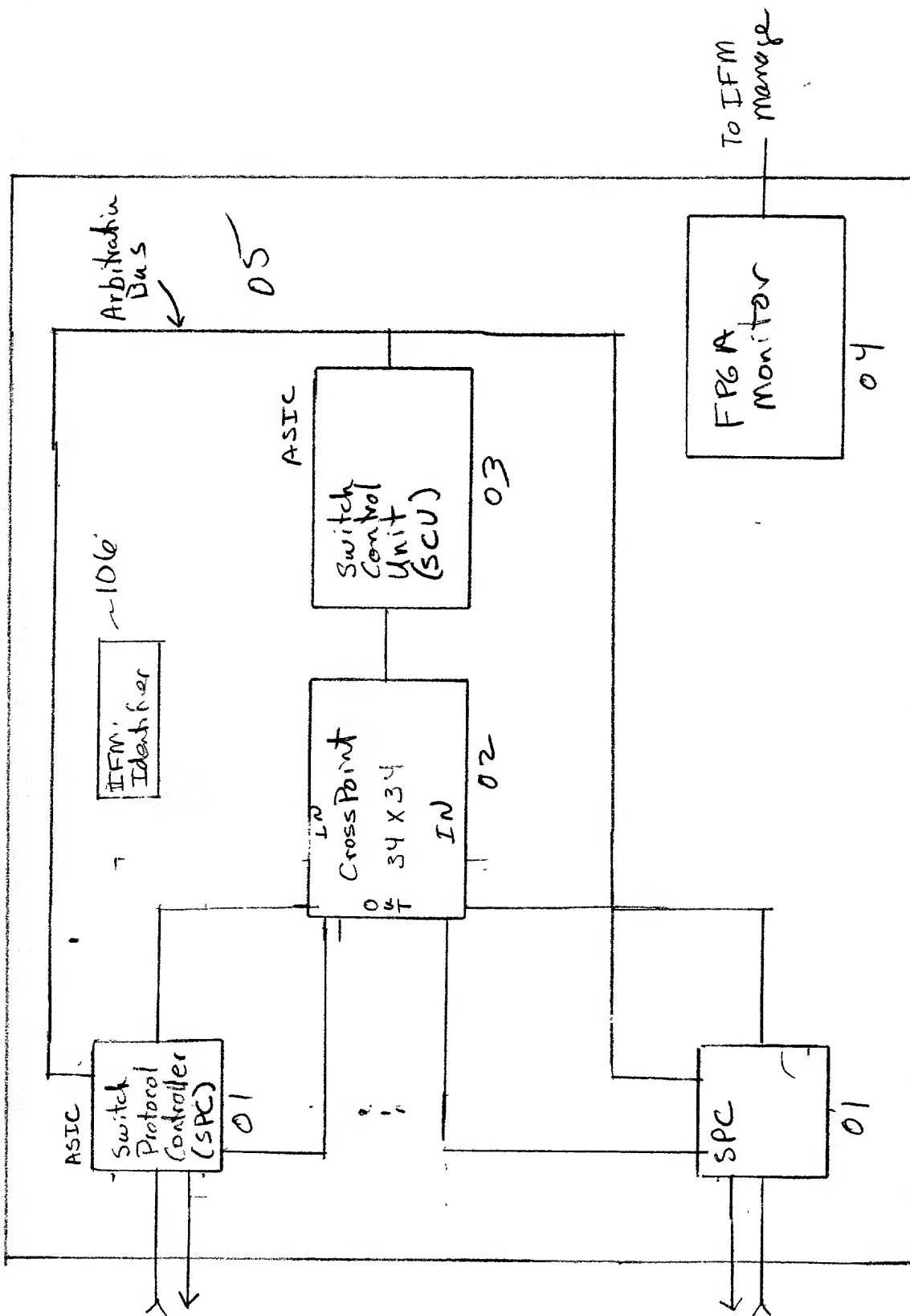


Fig 1

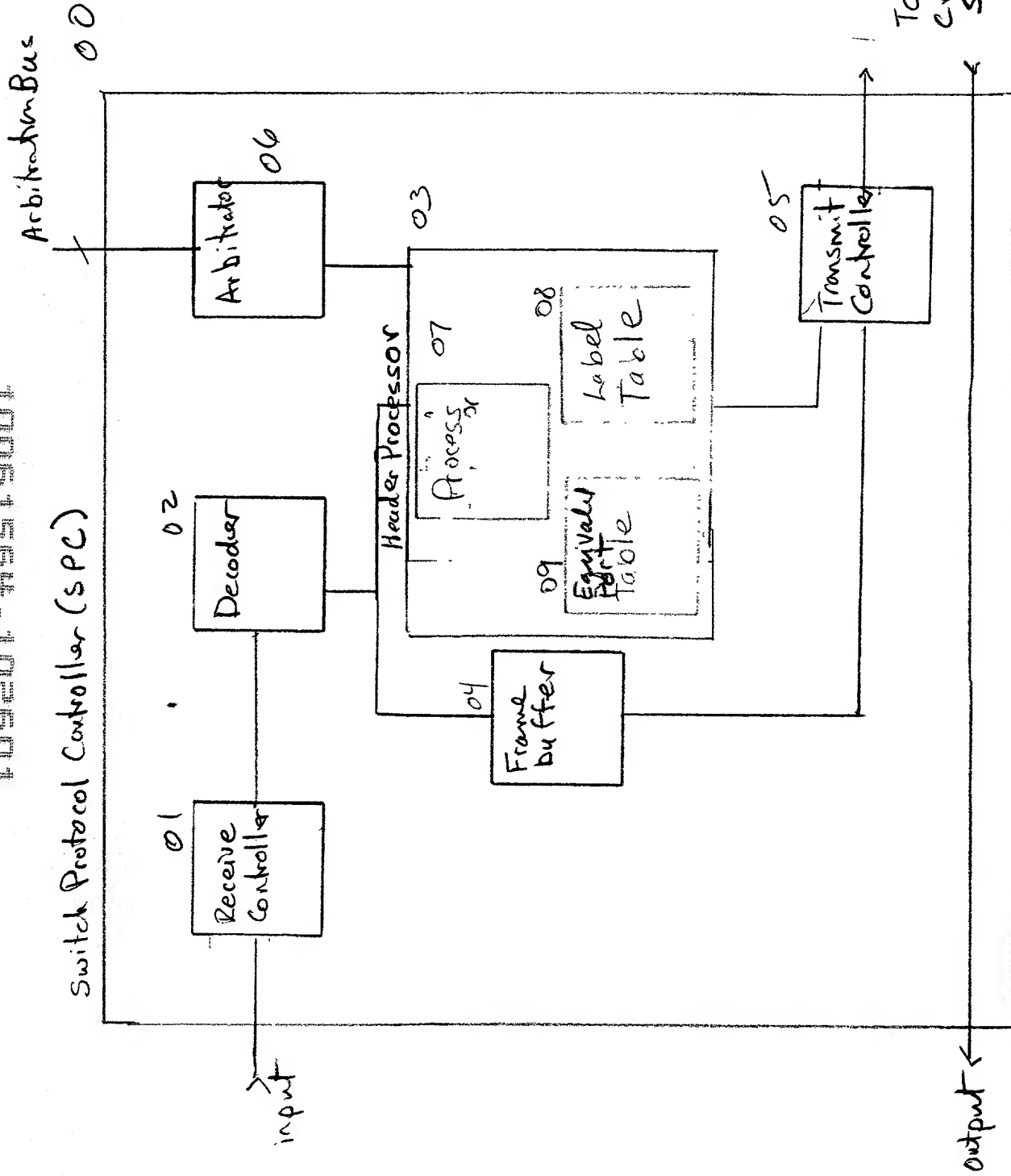


Fig 2

# Labelable Hierarchy

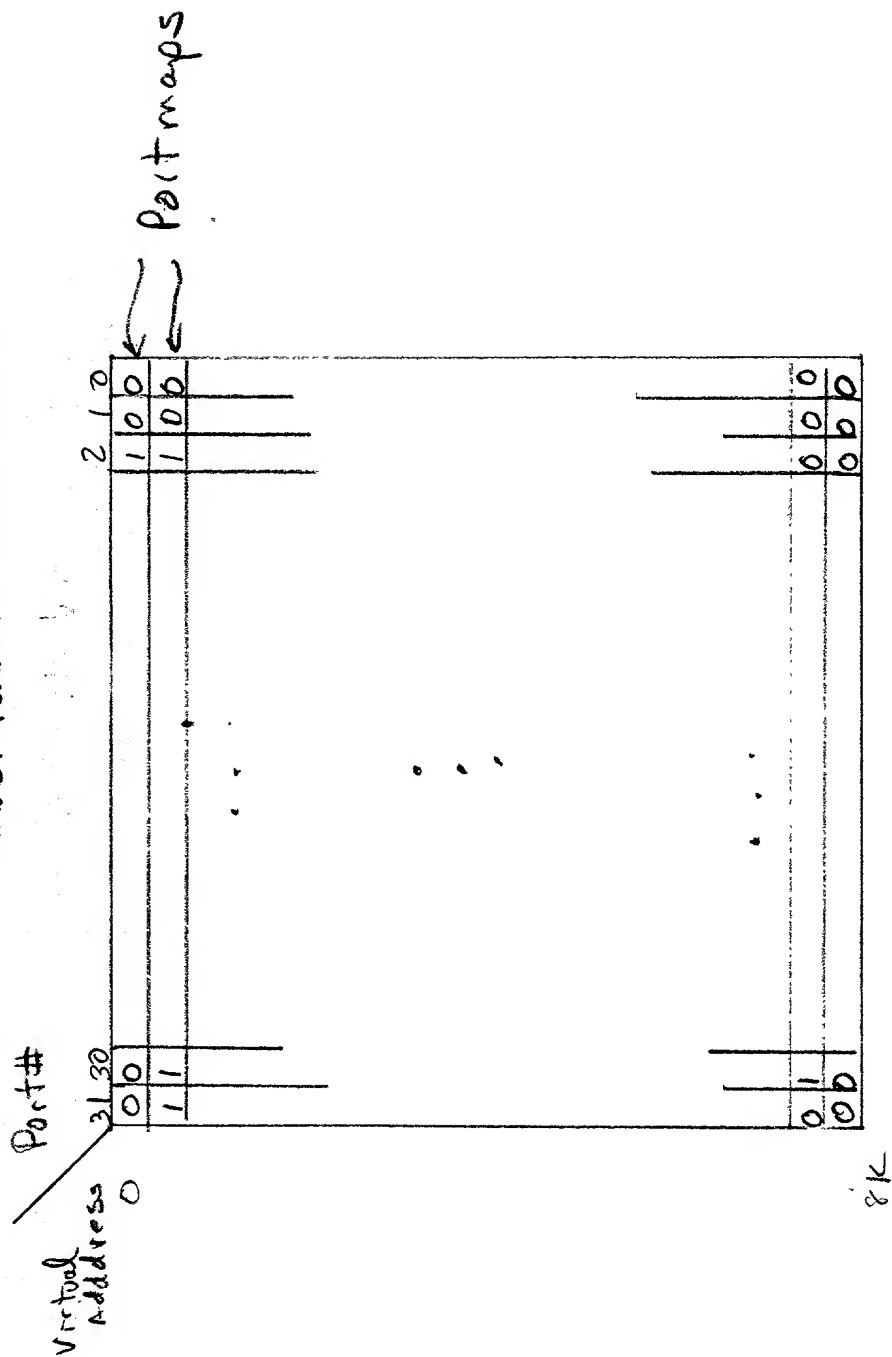


Fig 3

# Frame

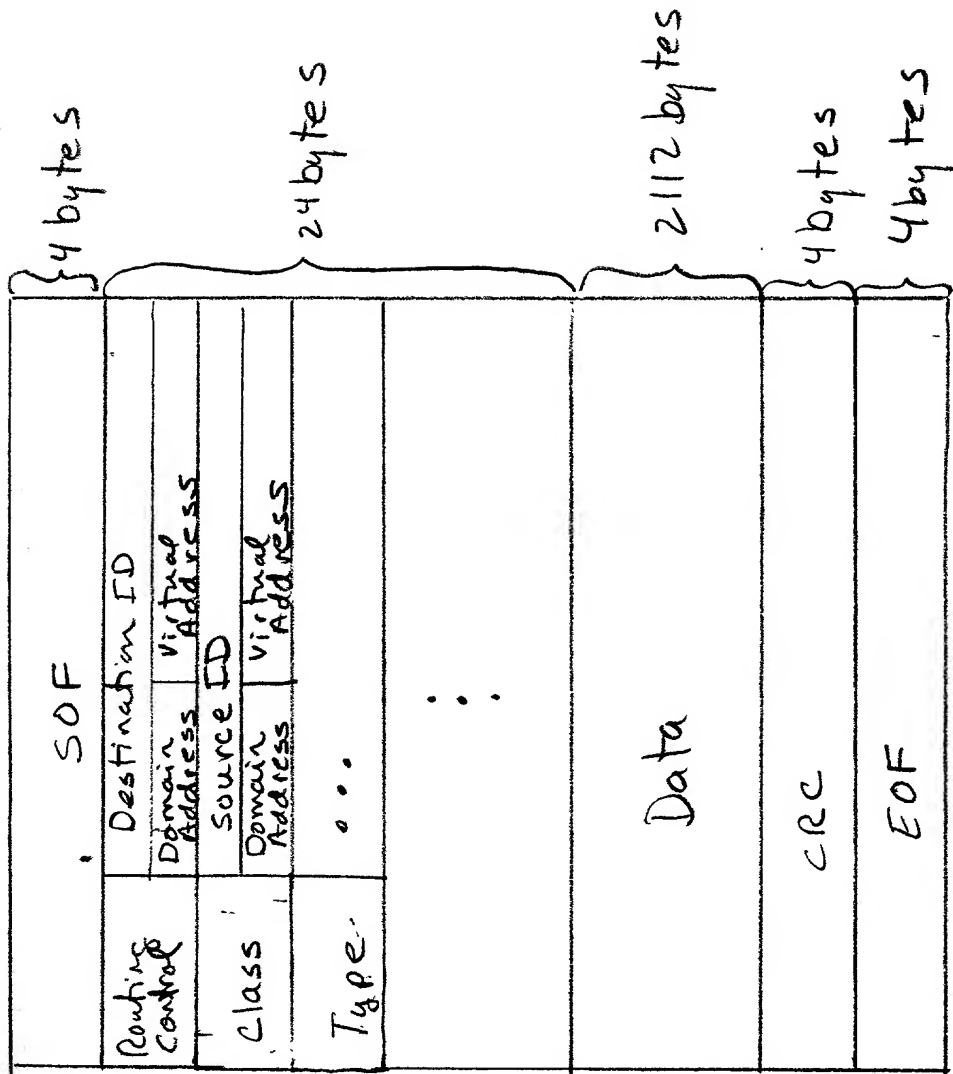


Fig 4

10061594-102601

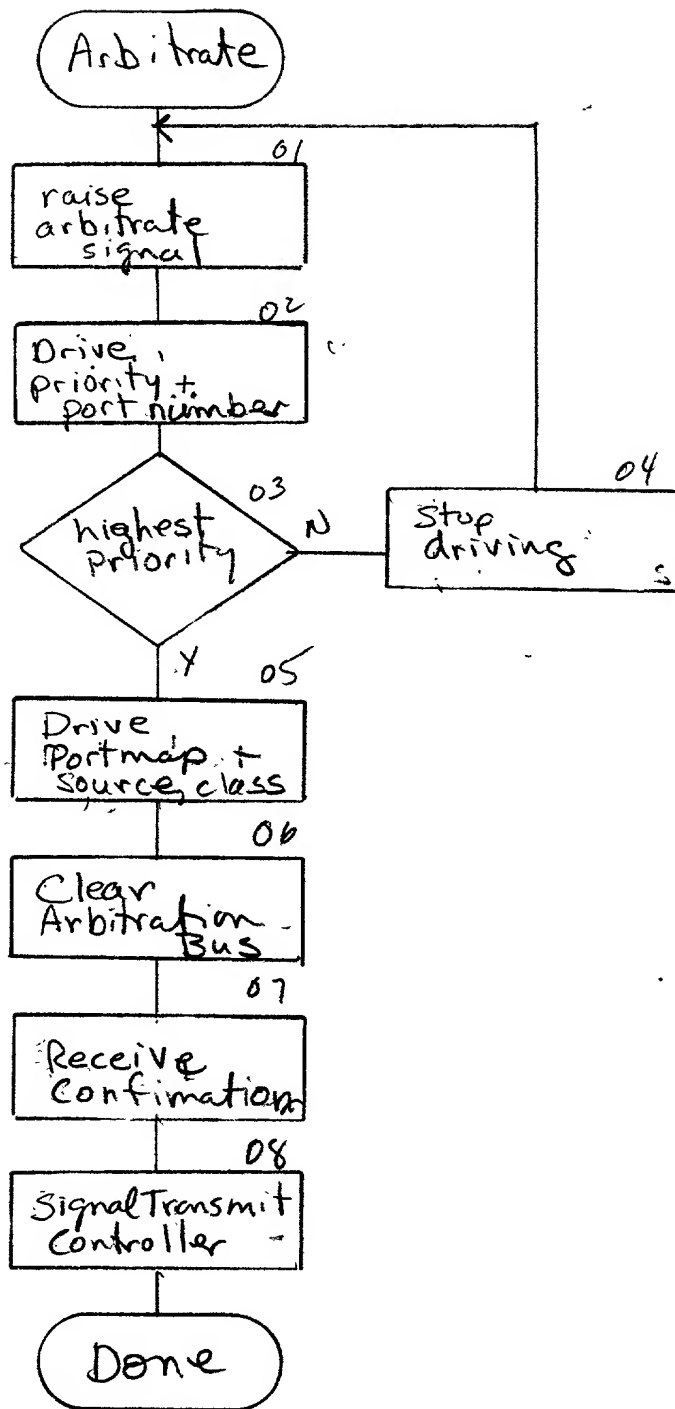


Fig 5

# Transmission Controller

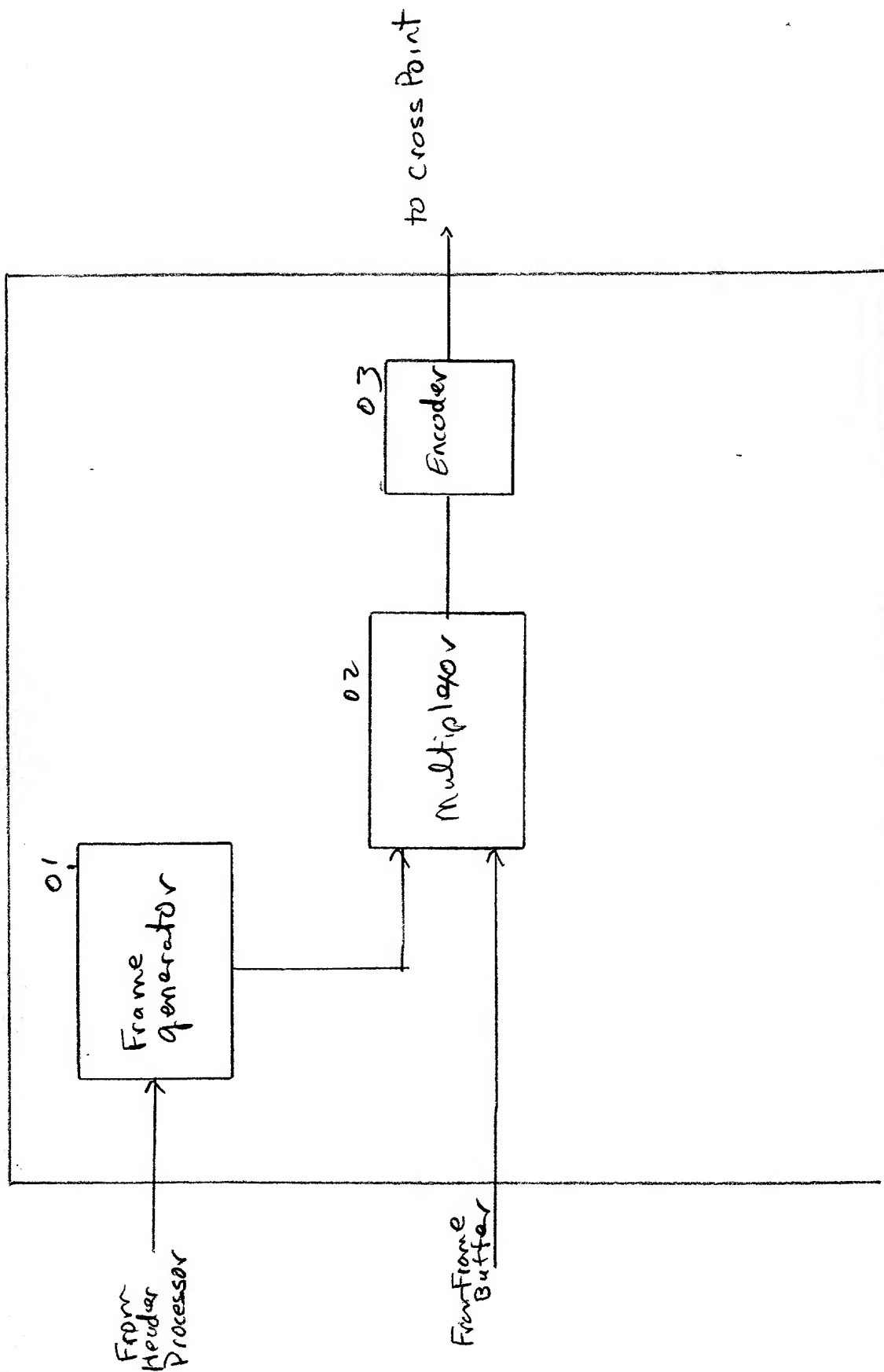


Fig 6

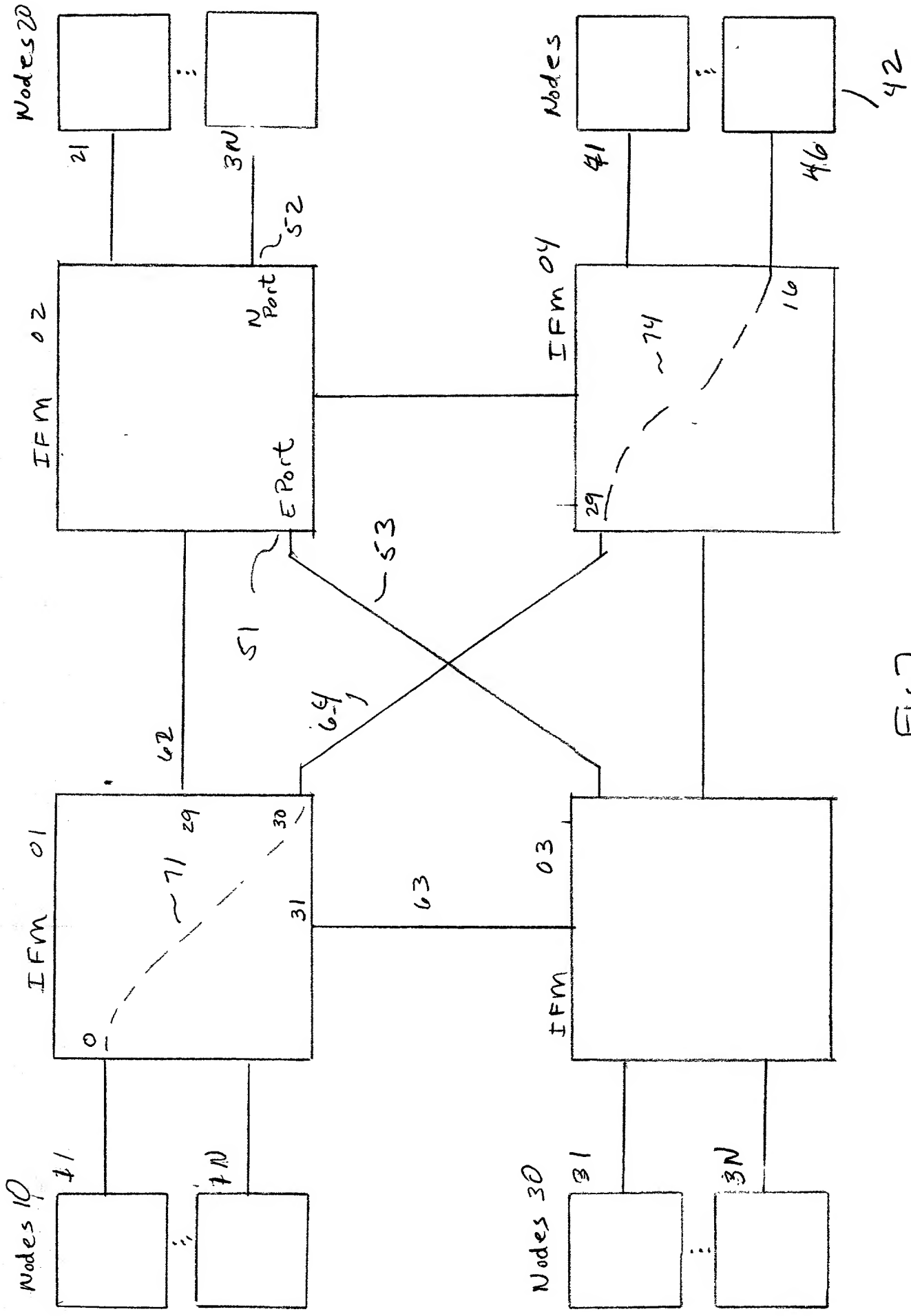


FIG. 7

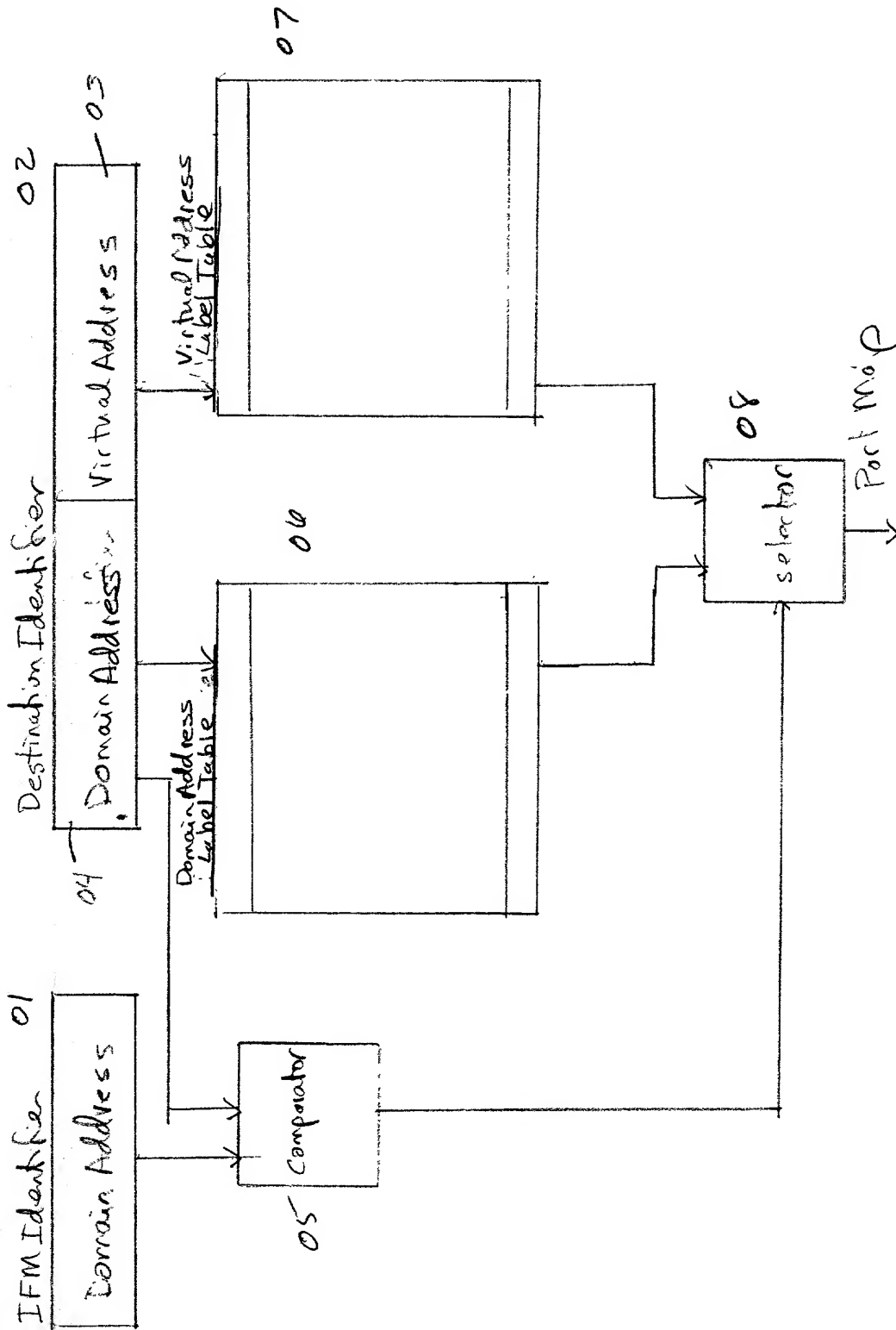


Fig. 8



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# Quad Switch Protocol Controller

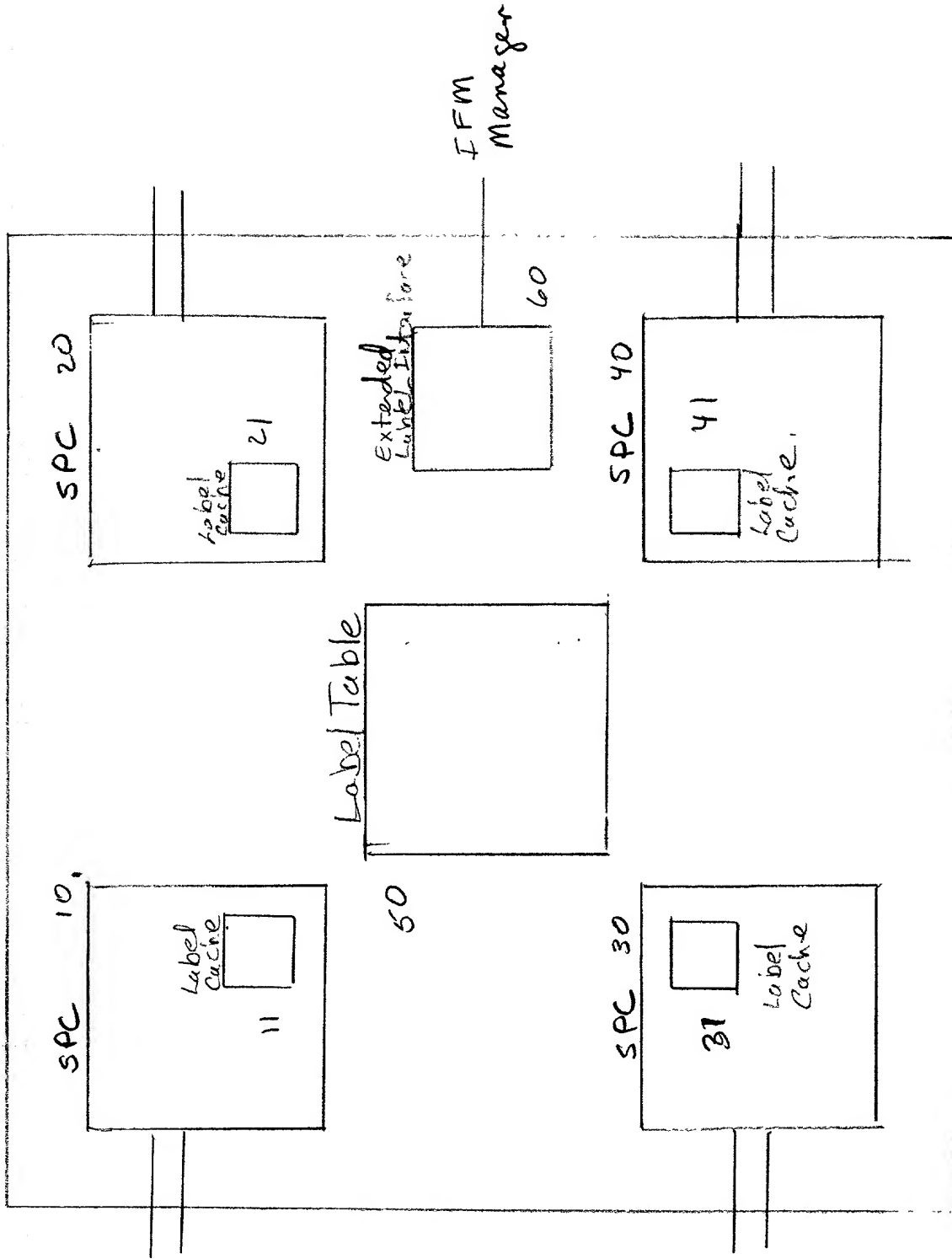


Fig 9

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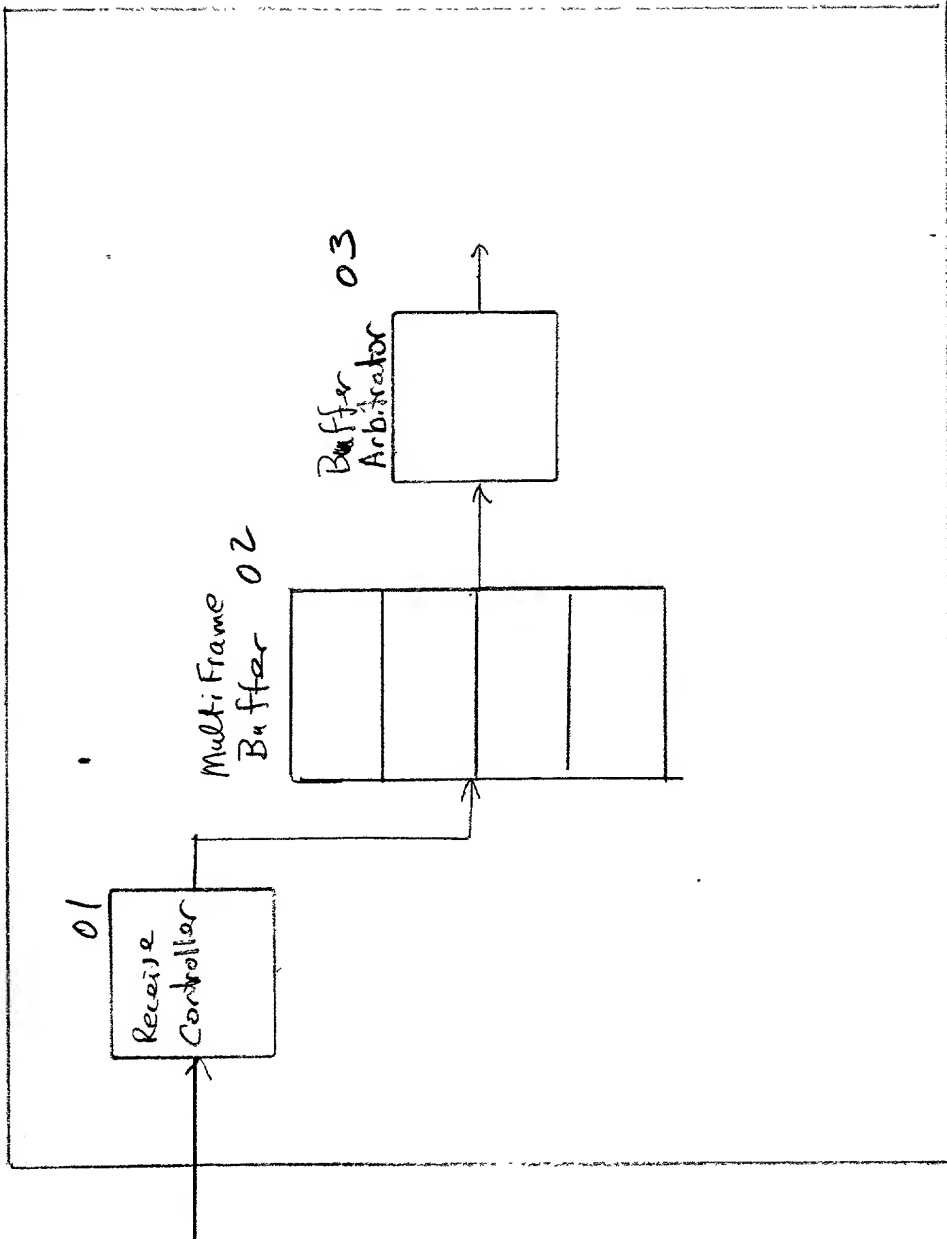


FIG 10

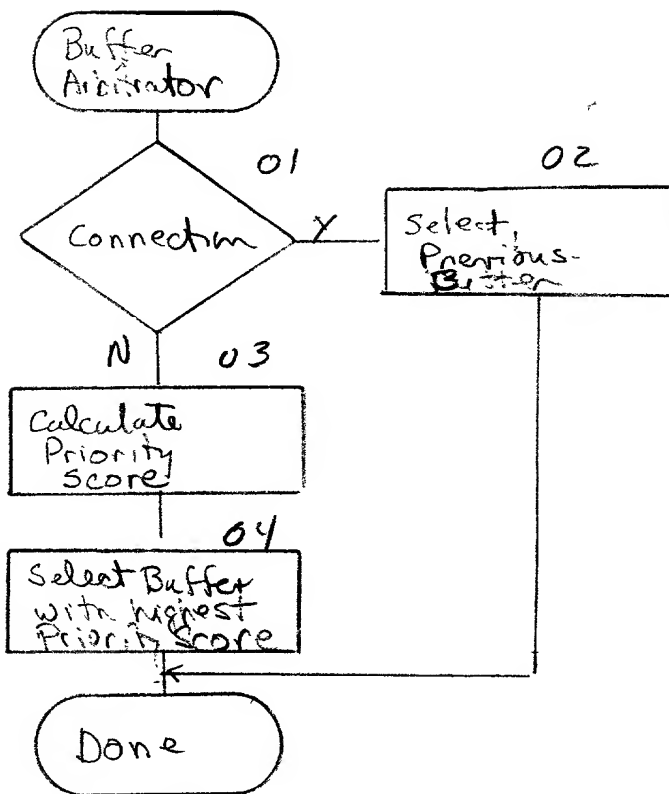


Fig 11

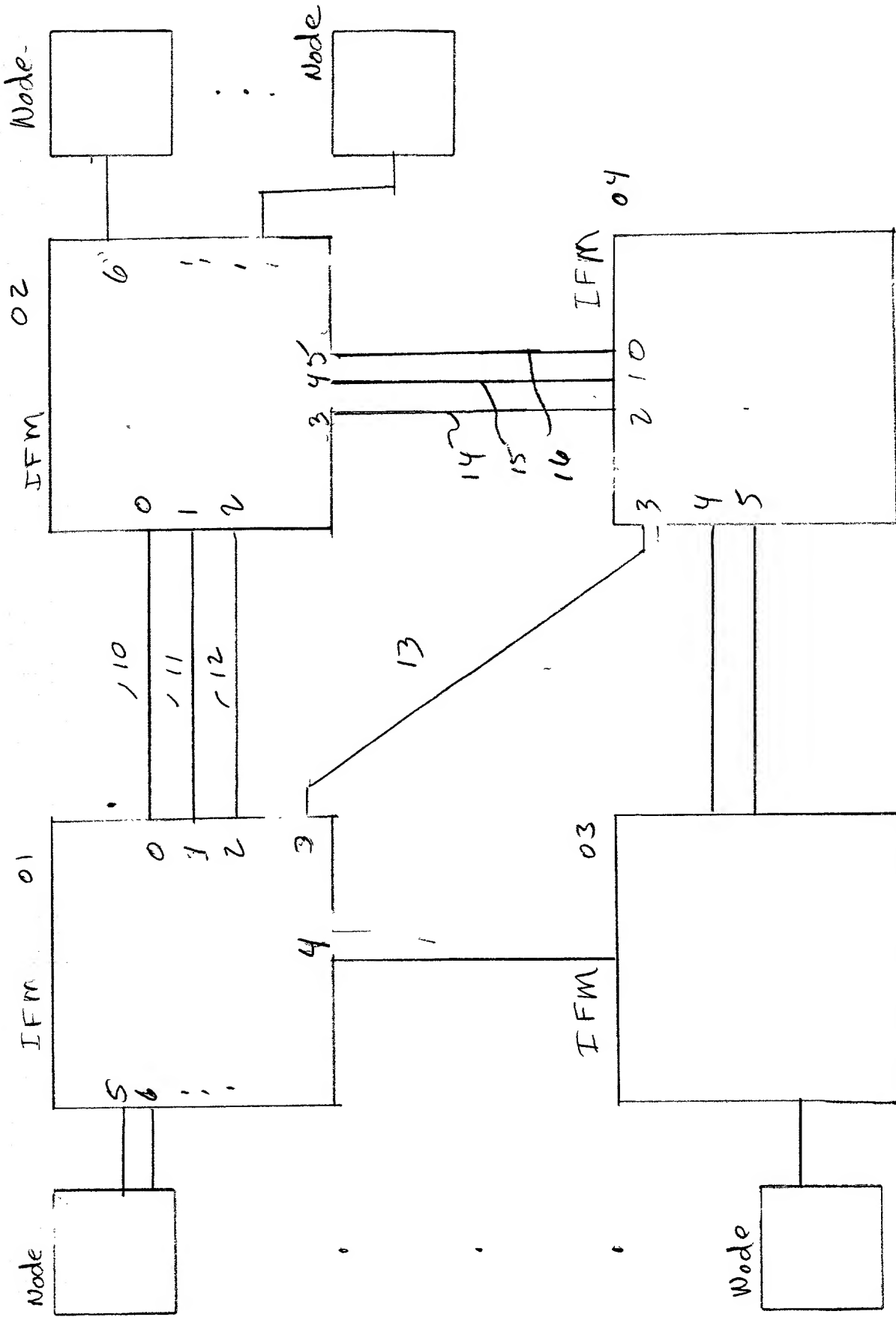


Fig 12



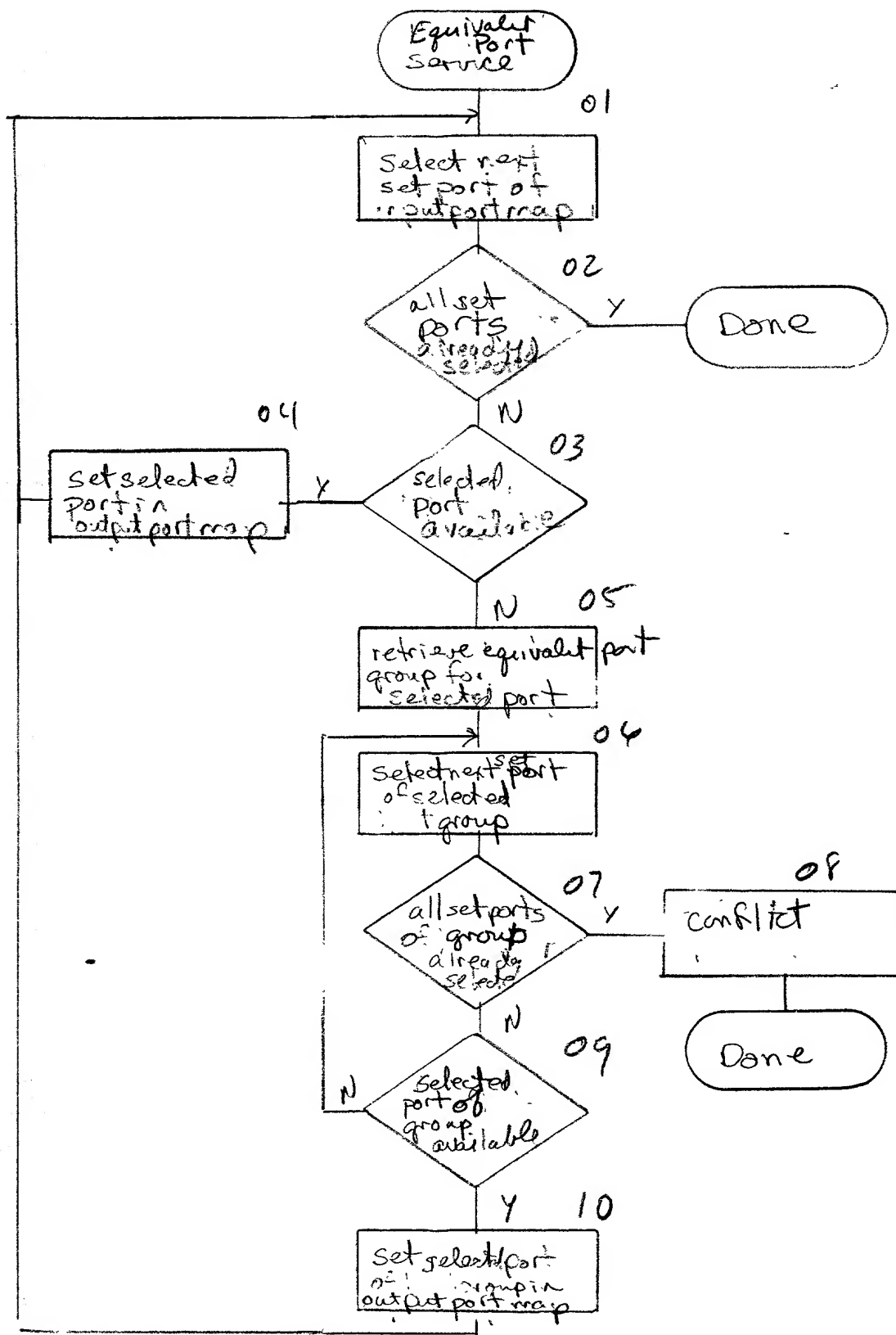


Fig 14

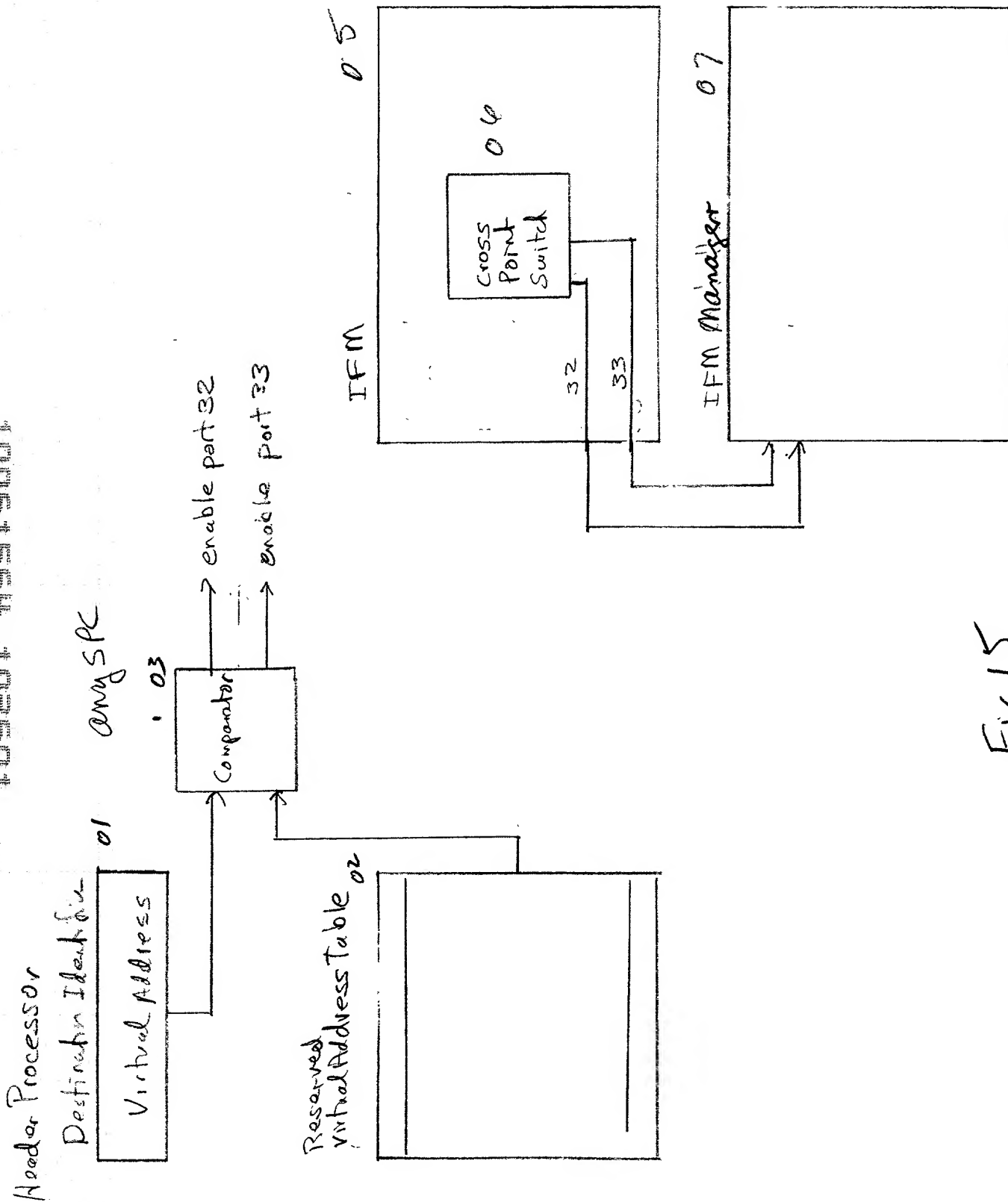
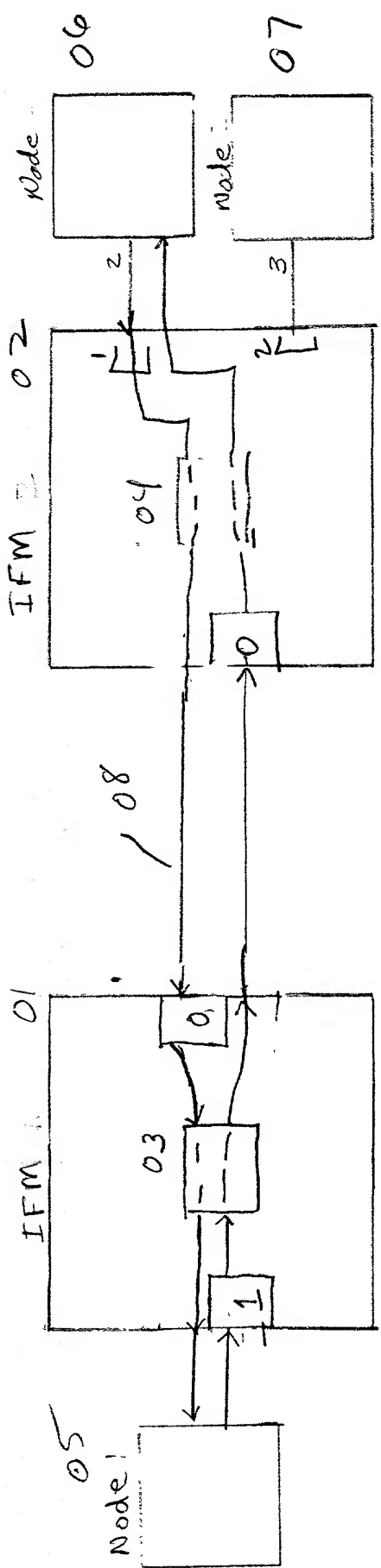


Fig 15



Deadlock 10

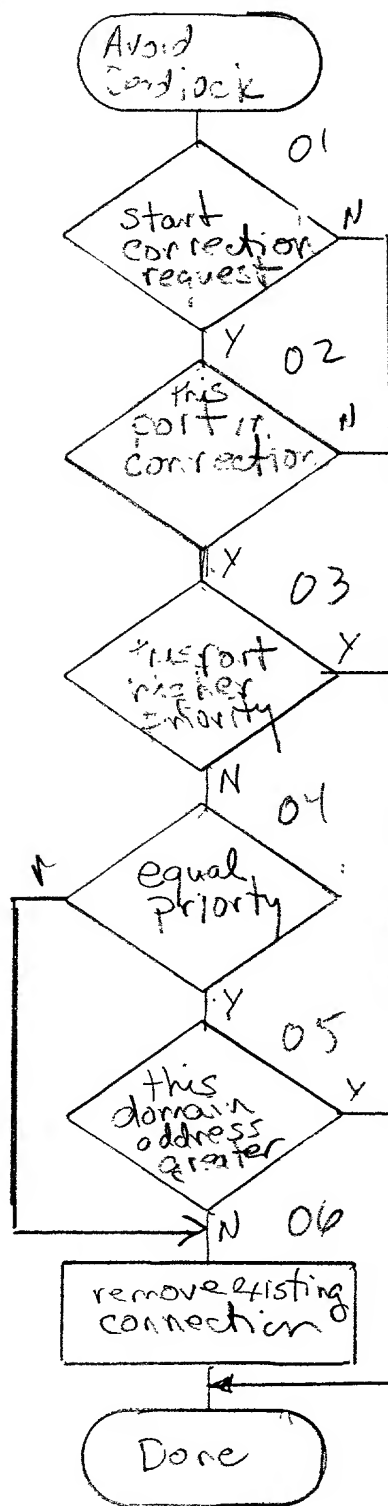
Time Node 1405 IFM 1601 Node 1606 IFM 1602  
 0 send start connect 1 3 send start connect 1 3

1	connect 1 ↔ 0	connect 2 ↔ 0
2	Forward start connect 1	Forward start connect 1
3	can't forward start connect node 1	can't forward start connect

Fig 16



1006154-10201



When end-to-end  
not established

Fig 17

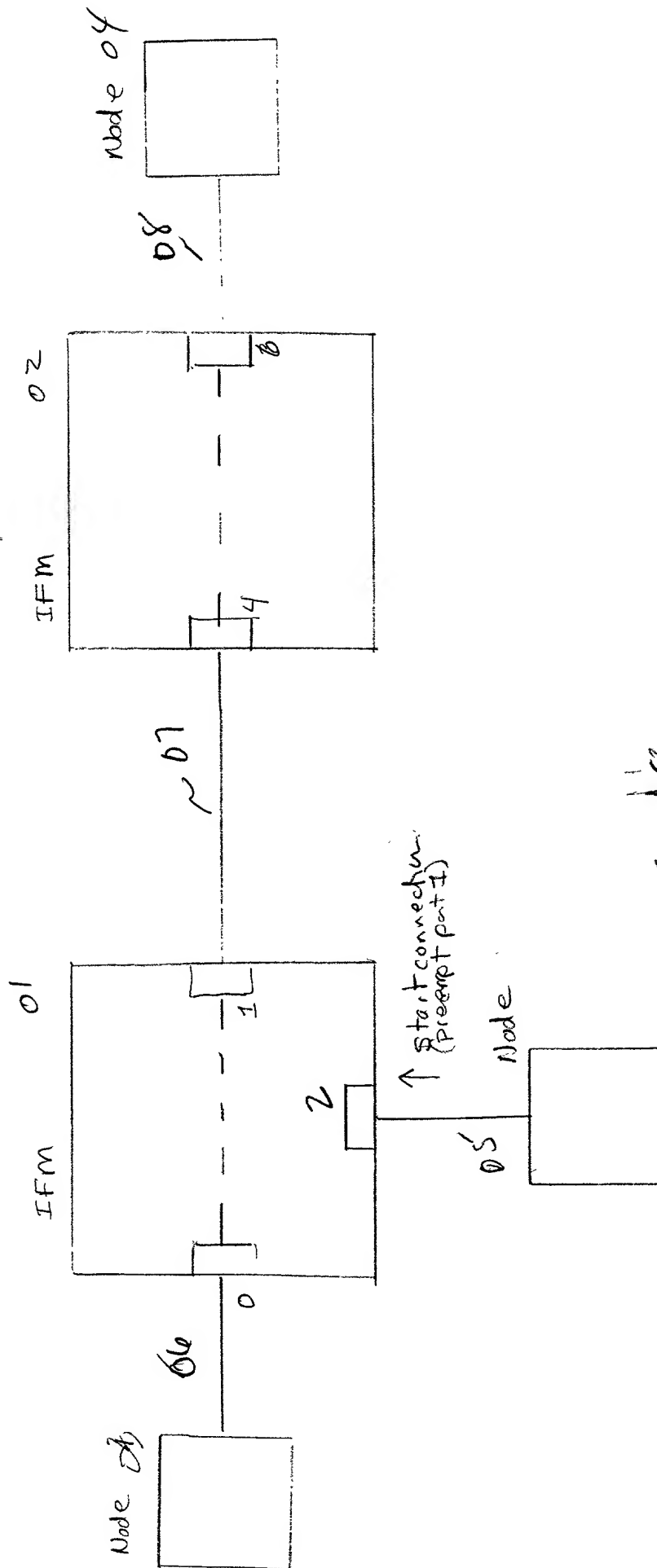


Fig 18

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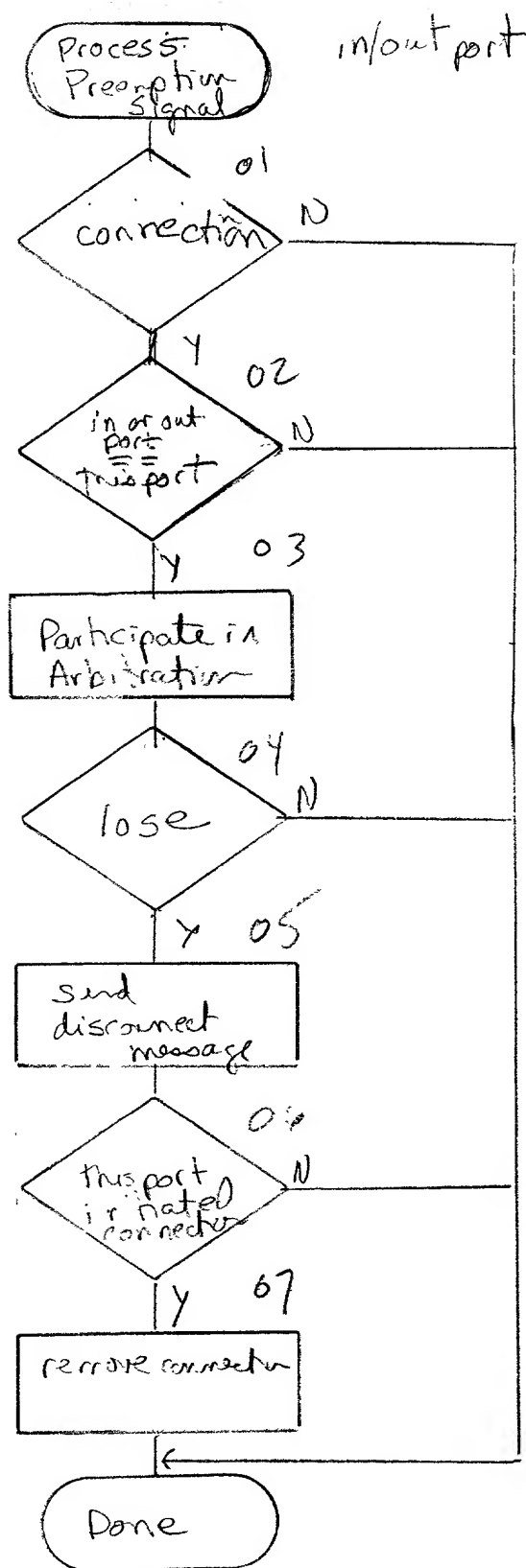


Fig. 19

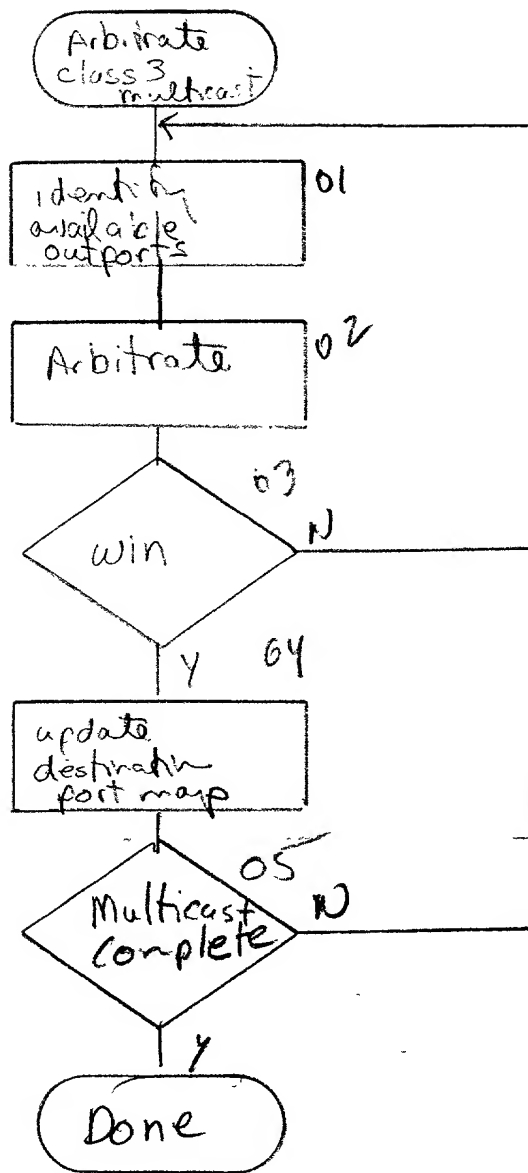


Figure 20